

EAST: [10663710 Self-aligned trench gate.wsp:1]

FileViewEditToolsWindowHelp

Drafts

BRS:

BRS:

BRS:

BRS: knall

BRS: 19 21 23

BRS: 32with 33

Pending

Active

L1: (8152) (trench\$2 recess\$4) near 2 gate

L2: (28943) self adj aligned

L3: (0) 10/663710

L4: (139) 2 near 3 1

L5: (2816) (insulat\$5 dielectric oxide) near (column pillar)

L6: (0) 4 same 5

L8: (1) 4 and 5

L7: (4) 1 same 5

L9: (1) 5 and 4

L10: (341) (257/332) CCLS

L11: (2) 10 and 5

L12: (880) (257/330) CCLS

L13: (14) 12 and 5

Failed

Saved

(1) 09/948877

(1738) "KONINKLIJKE PHILIPS".as

m110663710

Browse

Query

Clear

DB: USPAT:US POPUD:EP0: JPO: DERWENT: IBM: TDB

Plerats

Highlight all hit terms initially

Default operator: OR

12 and 5

Aug 2504

ASR term

ASK term

2 step

1 top

2 hit

	#	T	Inventor	Document#	Issue	P	Title	Current	Current X	Retrieval	S	C	P	3	4	5	Image	Doc	P
1			Inagawa, Hir	US 2002011	20020	2	Insulated gate type semiconductor device an	438/270	257/330		F	F	F	F	F	F	F	US 200201	F
2			Noble, Wend	US 2001002	20011	2	Circuits and methods using vertical, complem	257/330			F	F	F	F	F	F	F	US 200100	F
3			Noble, Wend	US 8777744	20040	2	Circuits and methods using vertical, complem	257/330	438/189		F	F	F	F	F	F	F	US 877774	F
4			Madson, Gor	US 6576954	20030	1	Trench MOSFET formed using selective epit	257/330	257/E21.41		F	F	F	F	F	F	F	US 657695	F
5			Furukawa, T	US 6440801	20020	7	Structure for folded architecture pillar mem	438/272	257/302		F	F	F	F	F	F	F	US 644080	F
6			Noble, Wend	US 8242775	20010	2	Circuits and methods using vertical complem	257/330	257/351		F	F	F	F	F	F	F	US 824277	F
7			Furukawa, T	US 6114725	20000	7	Structure for folded architecture pillar mem	257/330	257/286		F	F	F	F	F	F	F	US 611472	F
8			Burns, Jr., St	US 5990509	19991	6	2F-square memory cell for gigabit memory ap	257/296	257/316		F	F	F	F	F	F	F	US 599050	F
9			Park, Kyu-ch	US 5812559	19970	1	Semiconductor device having pillar shaped tr	257/302	257/301		F	F	F	F	F	F	F	US 581255	F
10			Shah, Pradee	US 5508544	18960	1	Three dimensional FAMOS memory devices	257/316	257/321		F	F	F	F	F	F	F	US 550854	F

Inc

Device

Doc title

Ready

NUM